



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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| In re Application of: Jin-Man Han |) | Examiner: Unknown |
| |) | |
| Serial No.: 10/634,268 |) | Group Art Unit: Unknown |
| |) | |
| Filed: August 5, 2003 |) | |
| |) | |
| For: Reference Cells for TCCT Based Memory |) | |
| Cells |) | |

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:


Listed on the attached Form PTO-1449 is information known to applicant. Each reference cited herein was previously cited in parent applications and a copy of each reference was provided to the Examiner, or cited by the Examiner in an Office Action. No additional copies of the references are being submitted with this paper, but will be furnished upon the Examiner's request.

Applicants respectfully request that the listed information be considered by the Examiner and be made of record in the above-identified application. If form PTO-1449 is enclosed, the Examiner is requested to initial and return it in accordance with MPEP 609.

This statement is not intended to represent that a search has been made or that the information cited in the statement is, or is considered to be, material to patentability as defined in section 1.56.

Respectfully submitted,

Date: October 10, 2003


Hark C. Chan (Reg. No. 35,477)

**Complete if Known**

(Use as many sheets as necessary)

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|-------|---|----|---|
| Sheet | 1 | of | 2 |
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|------------------------|----------------|
| Application Number | 10/634,268 |
| Filing Date | August 5, 2003 |
| First Named Inventor | Jin-Man Han |
| Art Unit | |
| Examiner Name | |
| Attorney Docket Number | P2341-G-1 |

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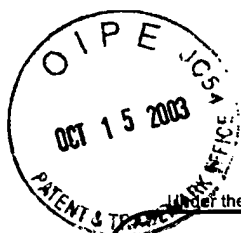
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PTO/SB/08B (08-03)

Approved for use through 07/31/2006. OMB 0651-0031

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|---|------|--------------------------|----------------|
| INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary) | | Complete if Known | |
| | | Application Number | 10/634,268 |
| | | Filing Date | August 5, 2003 |
| | | First Named Inventor | Jin-Man Han |
| | | Art Unit | |
| | | Examiner Name | |
| Sheet 2 | of 2 | Attorney Docket Number | P2341-C-1 |

| NON PATENT LITERATURE DOCUMENTS | | | |
|---------------------------------|-----------------------|---|----------------|
| Examiner Initials* | Cite No. ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T ² |
| | | HIRANDO, H., et al., 2V/100-ns 1T/1C Nonvolatile Ferroelectric Memory Architecture with Bitline-Driven Read Scheme and Nonrelaxation Reference Cell, IEEE Journal of Solid-State Circuits, 1997, pages 649-54, Vol. 32, No. 5 | |
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